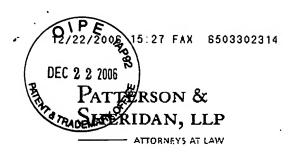


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TITLE:

ASYNCHRONOUS, INDEPENDENT AND MULTIPLE PROCESS SHARED MEMORY SYSTEM IN AN ADAPTIVE COMPUTING

ARCHITECTURE

U.S. SERIAL NO.:

10/644,621

FILING DATE:

August 19, 2003

INVENTOR:

Ric Howard

EXAMINER:

Kimberly N. McLean Mayo

GROUP ART UNIT:

2187

CONFIRMATION NO.:

7506

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